

U.S. PTO
10/04/2001
JC398

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10042732	FILING DATE 04/25/2001	CLASS 25748	SUBCLASS 618	GAU 2815	EXAMINER Thanh Nguyen
----------------------	---------------------------	----------------	-----------------	-------------	--------------------------

**APPLICANTS: Craig Sander; Rich Klein; Asim Selcuk; Nicholas Kepler; Christopher Spence; Raymond Lee; John Holst; Stephen Horne;

2813

**CONTINUING DATA VERIFIED:

This application is a DIV of 09/515,875 02/29/2000 PAT 6,287,953
which is a CIP of 09/119,934 07/21/1998 PAT 6,146,954

TN

Thanh
Nguyen
(2813)

** FOREIGN APPLICATIONS VERIFIED:

TN

PG-PUB	DO NOT PUBLISH <input checked="" type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiner's initials		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no
ATTORNEY DOCKET NO 039153-0433 (C167596-CIP)		
TITLE : Minimizing transistor size in integrated circuits		
U.S. DEPT. OF COMM./PAT. & TM-PTO-435L(Rev. 12-94)		

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
Amount Due	Date Paid		Total Claims	Print Claim for O.G.
TERMINAL		DRAWING		
		Sheets Drwg.	Figo.Drwg.	Print Fig.
DISCLAIMER		Application Examiner		
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.				

FILED WITH:

DISK (CRF)

CD-ROM
(Attached in pocket on right Inside flap)